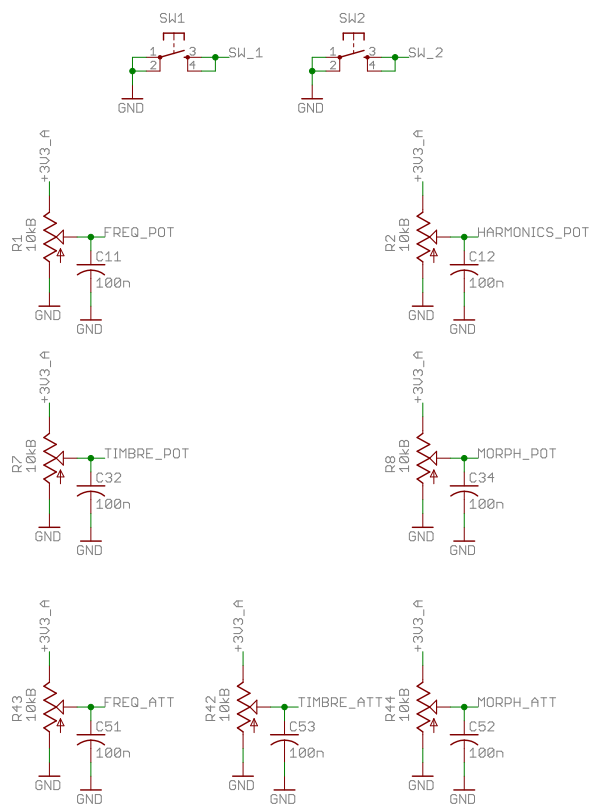
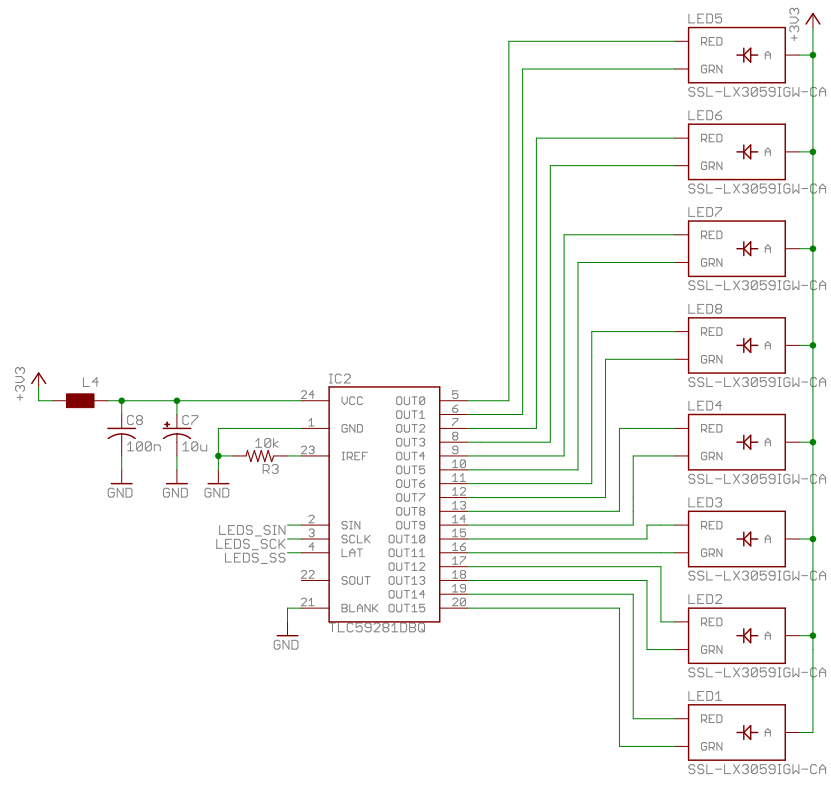


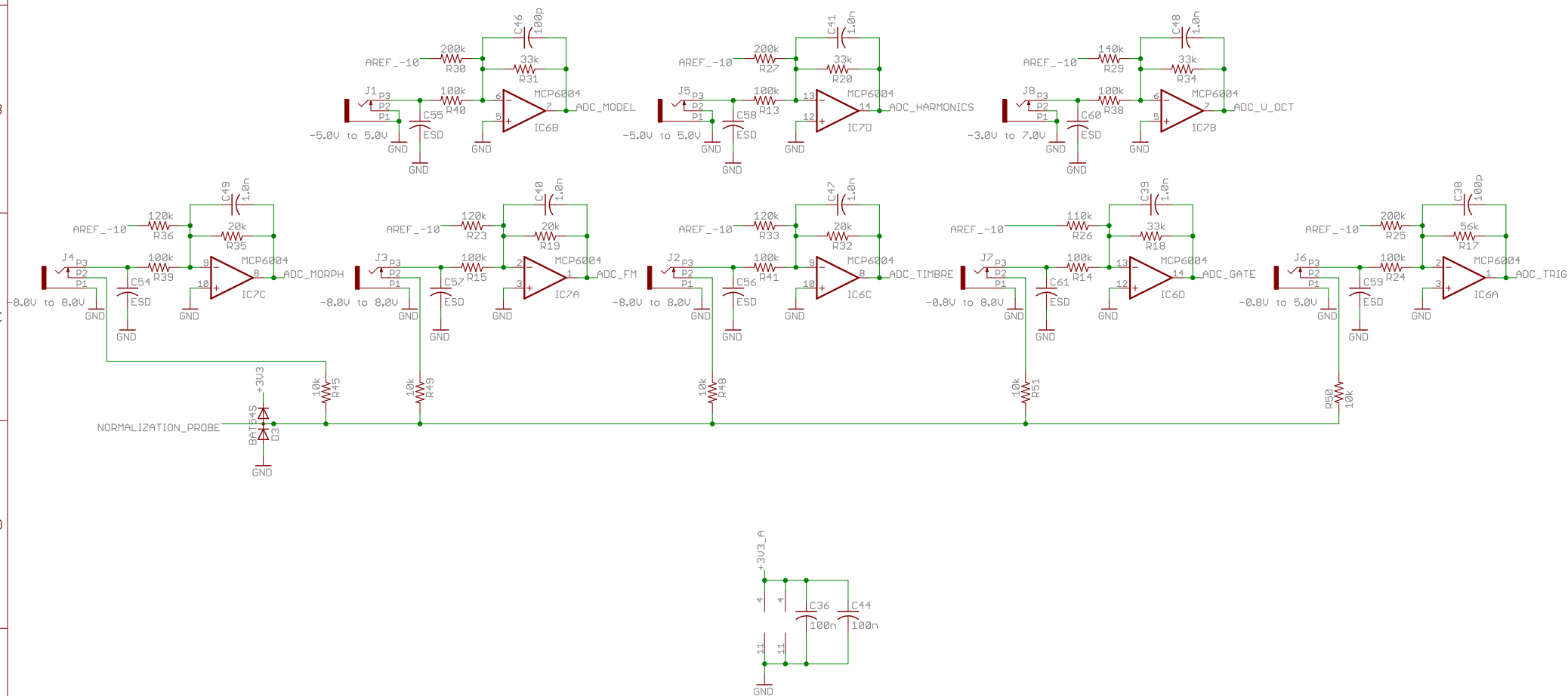
Factory testing interface



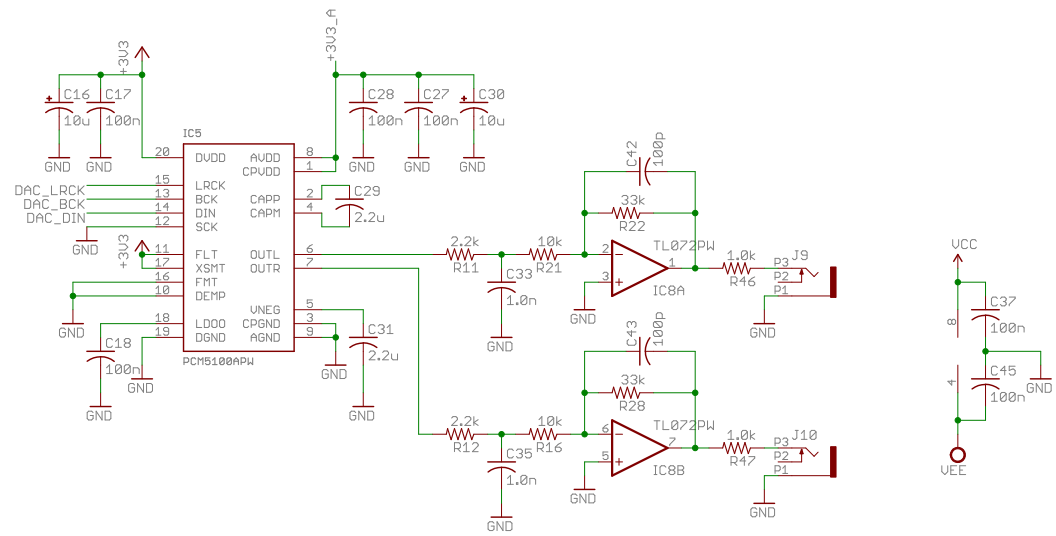
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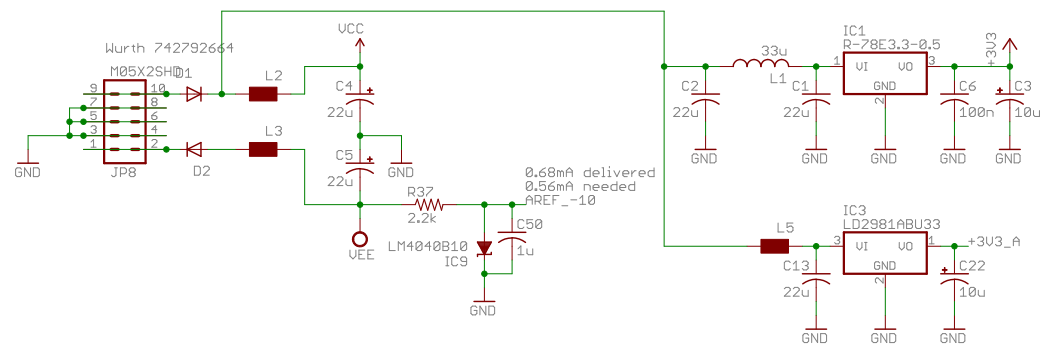
cc-by-sa	Controls
Émilie Gillet	plaits_v50
emilie.o.gillet@gmail.com	02/04/2019 22:05
	Sheet: 2/5

Optional ESD protection:
 1.0nF capacitor
 or:
 Ceradiode (eg TDK B72500D160H60, AEM ES0603U014CT)



cc-by-sa	CU inputs
Émilie Gillet	plaits_v50
emilie.o.gillet@gmail.com	02/04/2019 22:05
	Sheet: 3/5





H3 MOUNT-HOLE

H2 MOUNT-HOLE

cc-by-sa	Power
Émilie Gillet	plaits_v50
emilie.o.gillet@gmail.com	02/04/2019 22:05
Sheet: 5/5	